

ISCAS 2011 Lecture Session Schedule

Time	ALHAMBRA I	ALHAMBRA II	EL PARDO I	EL PARDO II	ORIENTE	IMPERIAL I	ALVORADA I	ALVORADA II
Monday May 16th, 2011 10:30-12:10	A1L-A Analog to Digital Converters I Chr: Viktor Gruev, John Harris Track: Analog Signal Processing	A1L-B Amplifiers Chr: Not Assigned Track: Analog Signal Processing	A1L-C Low-Dropout Regulator Chr: Viktor Gruev, Shih-Chih Liu Track: Analog Signal Processing	A1L-D Memory Circuits I Chr: Not Assigned Track: VLSI Systems, Architectures and Applications	A1L-E Life Science Systems & Applications Chr: Pau-Choo Julia Chung Track: Life-Science Systems and Applications	A1L-F Image & Video Coding Chr: Oscar C. Au Track: Visual Signal Processing and Communications	A1L-G Adaptive Techniques in Signal and Image Processing Chr: Not Assigned Track: Digital Signal Processing	A1L-H FIR Digital Applications Chr: Not Assigned Track: Digital Signal Processing
Monday May 16th, 2011 13:40-15:20	A2L-A Analog to Digital Converters II Chr: Wei Xing Zheng Track: Analog Signal Processing	A2L-B Low-Noise Amplifiers Chr: Not Assigned Track: Analog Signal Processing	A2L-C Emerging Energy & Power Integrated Circuits Chr: Hoi Lee, Gabriel Rincon-Mora Track: SPECIAL SESSIONS	A2L-D Memory Circuits II Chr: Not Assigned Track: VLSI Systems, Architectures and Applications	A2L-E Experiences with CAS teaching Chr: Tokunbo Ogunfunmi, Joos Vandewalle Track: Education in Circuits and Systems	A2L-F Visual Signal Analysis & Understanding Chr: Yap-Peng Tan Track: Visual Signal Processing and Communications	A2L-G Detection and Estimation Chr: Not Assigned Track: Digital Signal Processing	A2L-H Digital Filter Implementation Chr: Not Assigned Track: Digital Signal Processing
Monday May 16th, 2011 15:40-17:20	A3L-A Analog to Digital Converters III Chr: Wei Lu Track: Analog Signal Processing	A3L-B Wireless Circuits Chr: Piotr Dudek, Joachim Rodrigues Track: Analog Signal Processing	A3L-C Design Techniques for Storage Elements Chr: Not Assigned Track: VLSI Systems, Architectures and Applications	A3L-D VLSI for Video Systems I Chr: Not Assigned Track: VLSI Systems, Architectures and Applications	A3L-E Pedagogical Innovations in Circuits, Signals & Systems Education Chr: Babak Avazifar, Joos Vandewalle Track: SPECIAL SESSIONS	A3L-F Visual Signal Coding & Communications Chr: Shipeng Li Track: Visual Signal Processing and Communications	A3L-G Blind Signal Processing Chr: Wei Xing Zheng, Wei-Ping Zhu Track: Digital Signal Processing	A3L-H IC Implementation of DSP Algorithms Chr: Not Assigned Track: Digital Signal Processing

Experiences with CAS teaching Session

Session Type: Lecture
Session Code: A2L-E
Location: ORIENTE
Date & Time: Monday May 16, 2011 (13:40 - 15:20)
Chair: Tokunbo Ogunfunmi,
 Joos Vandewalle

● ● Papers are listed in the order they will be presented.

Add To My Sched	Paper Id	Topic	Title/Author
<input type="checkbox"/>	1206	15.1	Blended Engineering Course - Electric Circuit Theory Case Study <i>Jerry Rutkowski, Katarzyna Moscinska</i>
<input type="checkbox"/>	1616	15.2	Embedded Web Server for Remote Laboratory Access for Undergraduate Students Studying Electronic Engineering <i>Eugene Otoakhia, Tanaporn Jenmanachaiyakun, Ahmad Afaneh, Said Alze...</i>
<input type="checkbox"/>	2217	15.1	Education of Digital and Analog Circuits Supported by Computer Algebra System <i>Jan Kyncl, Martin Novotny</i>
<input type="checkbox"/>	2175	15.1	How to Teach Memristors in EE Undergraduate Courses <i>Jordi Albó Canals, Giovanni Paziienza</i>
<input type="checkbox"/>	2434	16.16	Nullators and Norators in Circuits Education: a Benefit or an Obstacle? <i>Joos Vandewalle, Josef Nosssek</i>

